

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) An apparatus, comprising:
a memory unit to store data;
a plurality of parallel data paths to process said data;
a plurality of control units to control said data paths; and
a switch to connect said control units to said data paths, said switch to receive configuration information to establish a first set of connections between said control units and said data paths to execute a first process using single instruction multiple data processing, and a second set of connections between said control units and said data paths to execute a second process using multiple instruction multiple data processing; wherein said first process and said second process may be performed substantially in parallel.

2. (Original) The apparatus of claim 1, wherein each control unit controls execution of a single program instruction.

3. (Cancelled).

4. (Original) The apparatus of claim 2, wherein said first set of connections connect at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths.

5. (Original) The apparatus of claim 4, wherein each data path performs a same set of operations using said data.

6. (Cancelled).

7. (Original) The apparatus of claim 2, wherein said second set of connections connect multiple control units to multiple data paths, with each control unit to control a single data path.

8. (Original) The apparatus of claim 4, wherein each data path performs a different set of operations using said data.

9. (Original) The apparatus of claim 1, further comprising a configuration module to configure said switch to establish said connections in accordance with said configuration information.

10. (Currently Amended) A system, comprising:

an antenna;

a host processing system;

a configuration module to store configuration information; and
a reconfigurable communication architecture module to receive said configuration information, said reconfigurable communication architecture module to configure itself to perform single instruction multiple data processing in a first configuration to execute a first process, and to perform multiple instruction multiple data processing in a second configuration to execute a second process; wherein said first process and said second process may be performed substantially in parallel.

11. (Original) The system of claim 10, wherein said reconfiguration communication architecture module comprises:

a plurality of processing elements to execute functions for each process;
a plurality of routing elements to connect said processing elements; and
a plurality of communications mediums to connects said processing elements and said routing elements in a mesh topology.

12. (Original) The system of claim 10, wherein one of said processing elements comprises:

a memory unit to store data;
a plurality of parallel data paths to process said data;
a plurality of control units to control said data paths; and
a switch to connect said control units to said data paths, said switch to receive said configuration information to establish a first set of connections between said control units

and said data paths to execute said first process, and a second set of connections between said control units and said data paths to execute said second process.

13. (Original) The system of claim 12, wherein each control unit controls execution of a single program instruction.

14. (Original) The system of claim 13, wherein said first set of connections connect at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths.

15. (Original) The system of claim 13, wherein said second set of connections connect multiple control units to multiple data paths, with each control unit to control a single data path.

16. (Currently Amended) A method, comprising:
receiving configuration information at a switch; and
configuring said switch to establish a first set of connections between a plurality of control units and a plurality of data paths to execute a first process using single instruction multiple data processing; and
configuring said switch to establish a second set of connections between said control units and said data paths to execute a second process using multiple instruction multiple data processing;

wherein said first process and said second process may be performed substantially in parallel.

17. (Original) The method of claim 16, wherein each control unit controls execution of a single program instruction.

18. (Original) The method of claim 17, wherein said first set of connections connect at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths.

19. (Original) The method of claim 17, wherein said second set of connections connect multiple control units to multiple data paths, with each control unit to control a single data path.

20. (Original) The method of claim 16, further comprising:
receiving a first set of data;
storing said first set of data in a memory unit; and
processing said first set of data with said data paths using said first set of connections.

21. (Original) The method of claim 16, further comprising:
receiving a second set of data;
storing said second set of data in a memory unit; and

processing said second set of data with said data paths using said second set of connections.

22. (Currently Amended) An article comprising:

a storage medium;

said storage medium including stored instructions that, when executed by a processor, result in receiving configuration information at a switch, configuring said switch to establish a first set of connections between a plurality of control units and a plurality of data paths to execute a first process using single instruction multiple data processing, and configuring said switch to establish a second set of connections between said control units and said data paths to execute a second process using multiple instruction multiple data processing; wherein said first process and said second process may be performed substantially in parallel.

23. (Original) The article of claim 22, wherein the stored instructions, when executed by a processor, further result in said first set of connections connecting at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths.

24. (Original) The article of claim 22, wherein the stored instructions, when executed by a processor, further result in said second set of connections connecting multiple control units to multiple data paths, with each control unit to control a single data path.